

S / Suppl.
IDS
e. Willis
7-5-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

B.S. Beaman et al.

Serial No.: 09/382,834

Filed: August 25, 1999

For: HIGH DENSITY INTEGRATED CIRCUIT APPARATUS, TEST PROBE AND METHODS OF USE THEREOF

Date: July 3, 2001

Group Art Unit: 2858

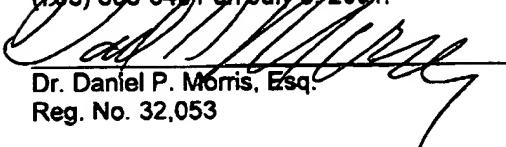
Examiner: V. Nguyen

Docket No.: YO993-028BX

Assistant Commissioner for Patents
Washington, D.C. 20231

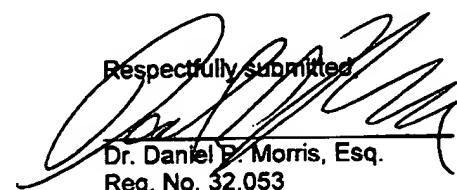
CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper (1 page) and translation (5 pages) are being facsimile transmitted under Rule 37 CFR 1.61(d) to the U.S. Patent and Trademark Office to (703) 305-3431 on July 3, 2001.


Dr. Daniel P. Morris, Esq.
Reg. No. 32,053SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL LETTER

Sir:

The Information Disclosure Statement (IDS) and Form PTO-1449 submitted on June 11, 2001 included Japanese Patent document PEPA 45-29855, which was cited in a parent application, of the present application. Enclosed herewith is a translation of PEPA 45-29855. Please charge any fee necessary to enter this paper to deposit account 09-0468.


Respectfully submitted,Dr. Daniel P. Morris, Esq.
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